

READ CYCLE FROM USB (FIFO) & WRITE CYCLE TO EXTERNAL MEMORY 2 DA2 5 DA1 TRANSFER REQUEST RECEIVE DATA (FIFO UNIT OUTPUT) ADDRESS (2A) DATA (SDRAM INPUT) Trreo : CLOCK FIFO READ INSTRUCTION COMMAND (2A) USBBREQ USBBACK FIFO COUNTER

FIG. 3

0,0

FIG. 4

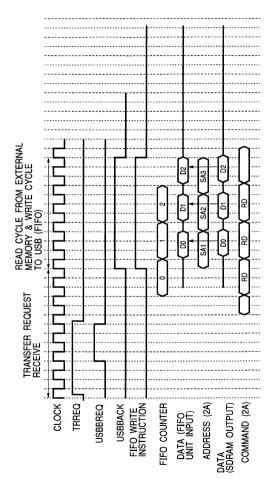
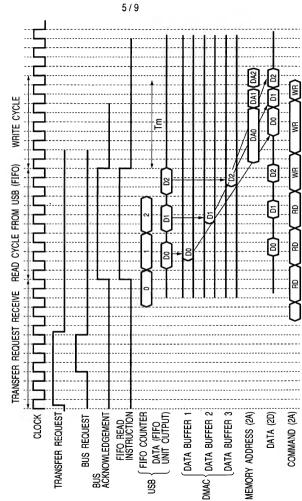


FIG. 5



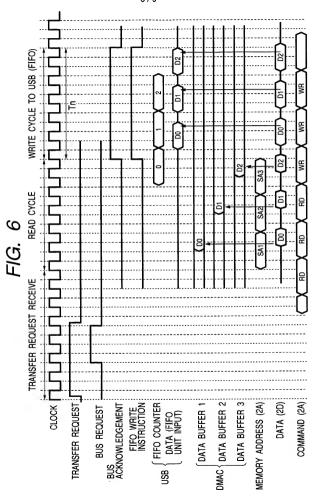


FIG. 7

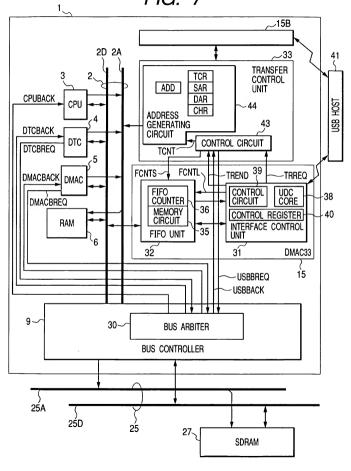
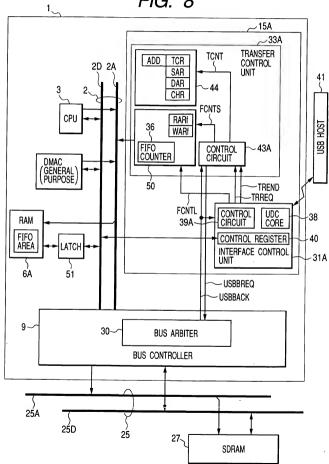
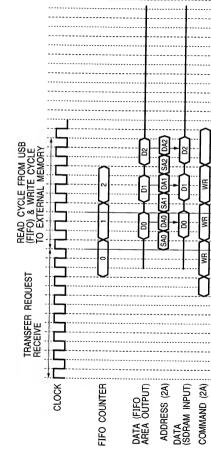


FIG. 8





5

FIG. 9